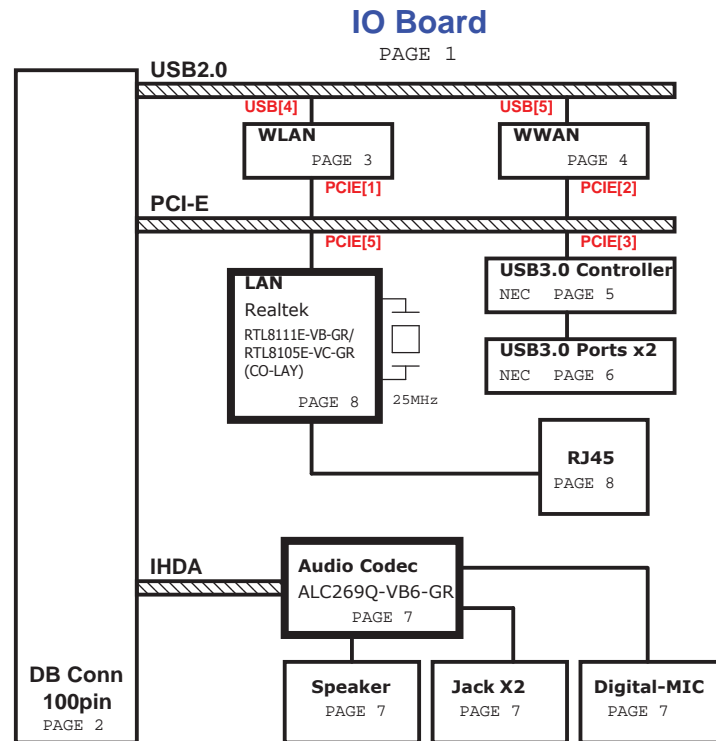
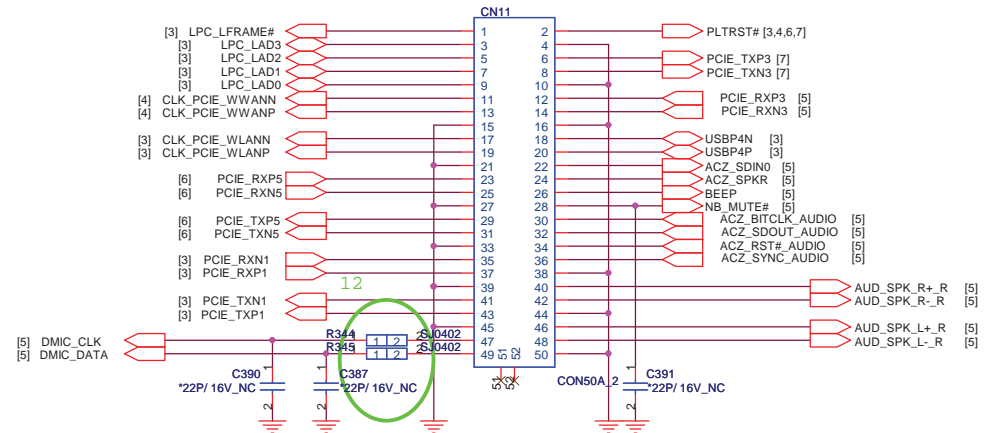
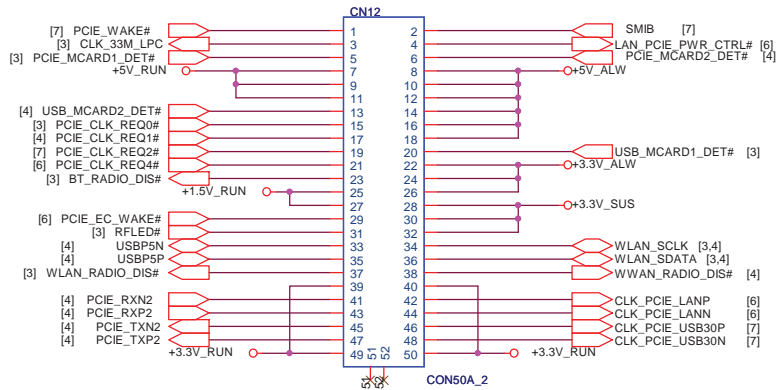


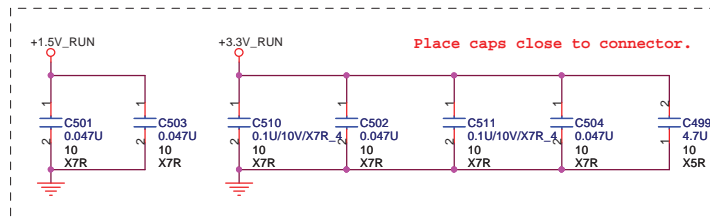
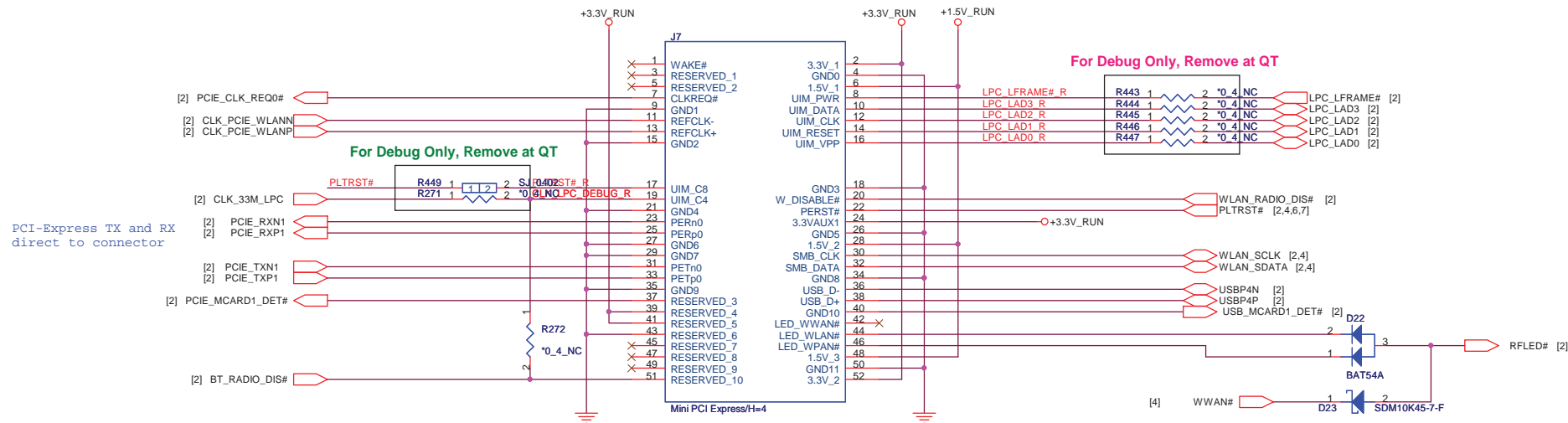
R01/R01A/V02/V02A

I/O Board Block Diagram

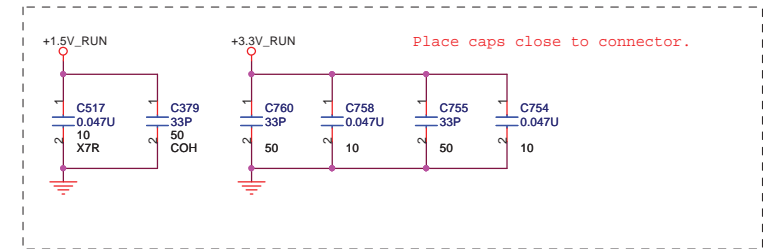
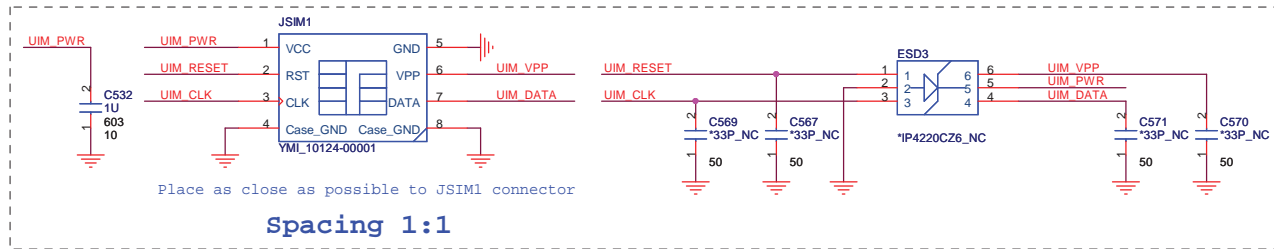
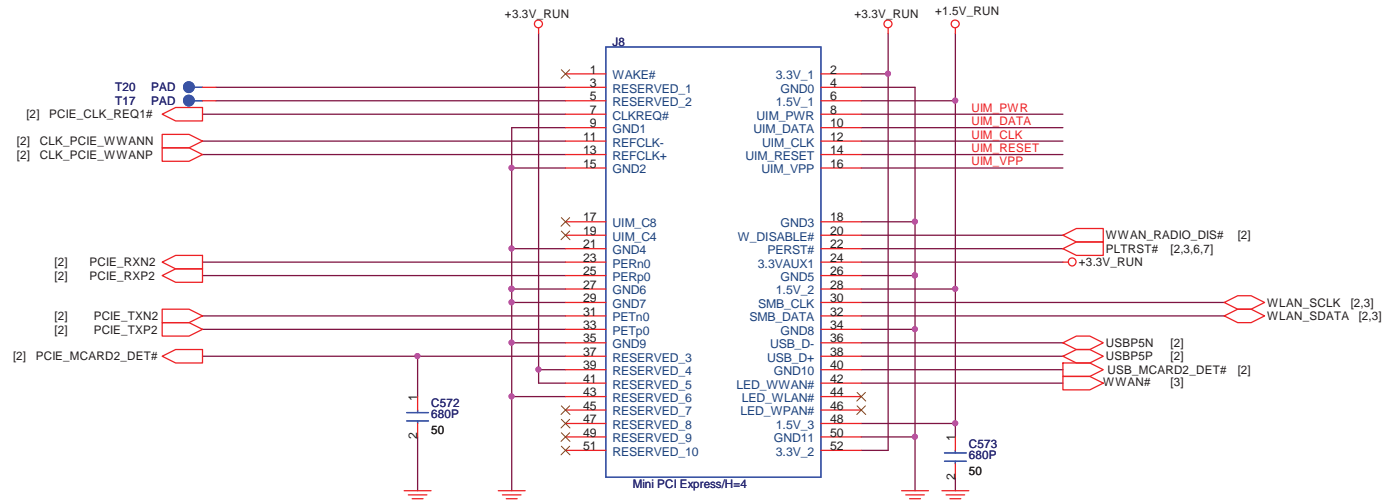




MiniCard WLAN connector



MiniCard WWAN connector



Quanta Computer Inc.

PROJECT : R01/R01A/V02/V02A

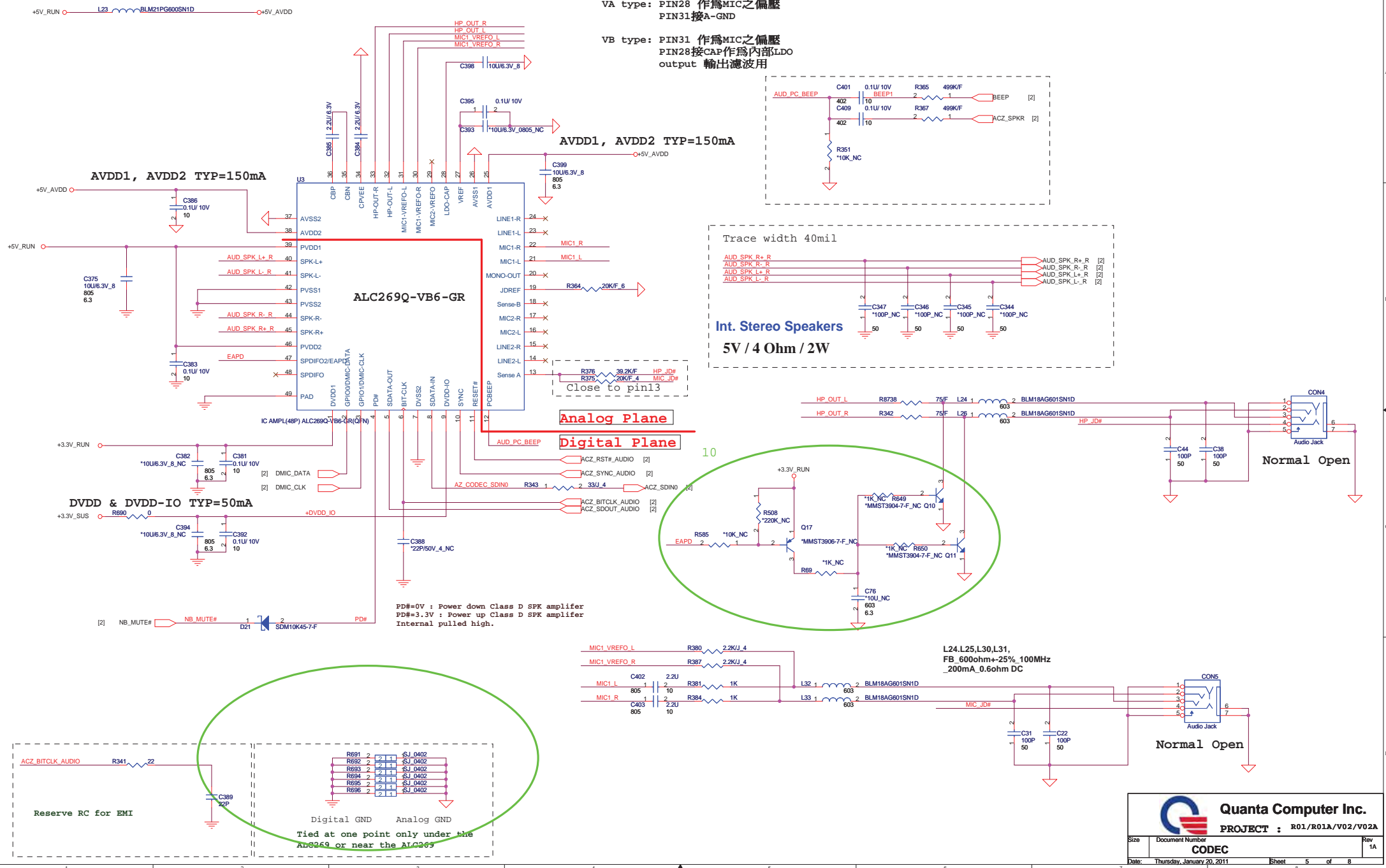
Size Document Number MINI-PCI (WWAN) Rev 1A

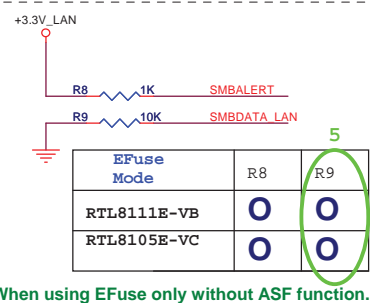
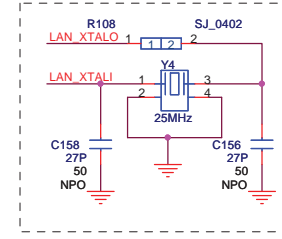
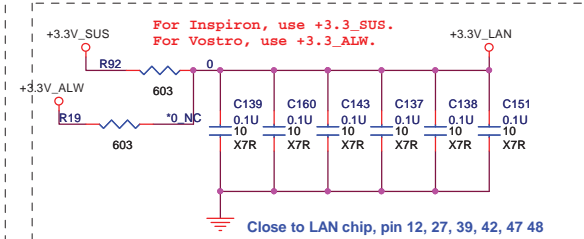
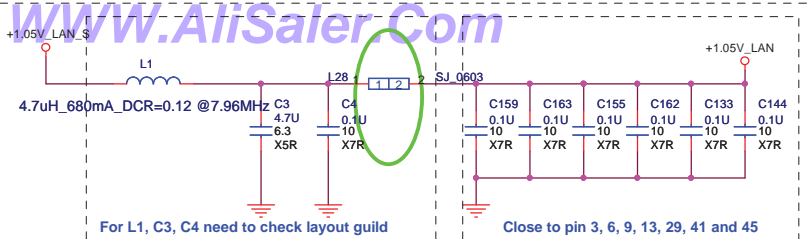
Date: Thursday, January 20, 2011 Sheet 4 of 8

*NOTE: ALC269_VB type add the LDO circuit in IC

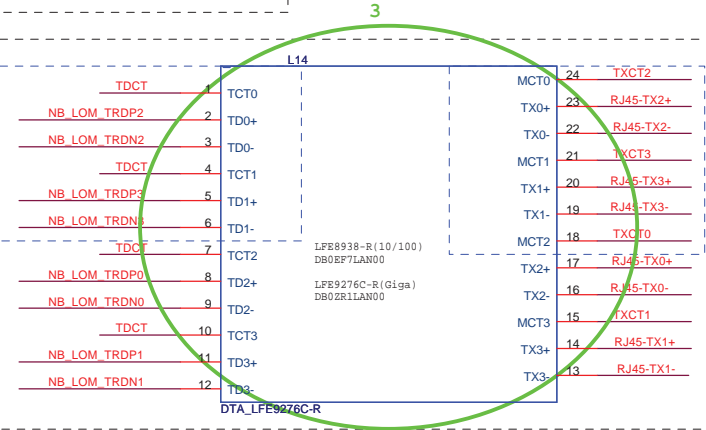
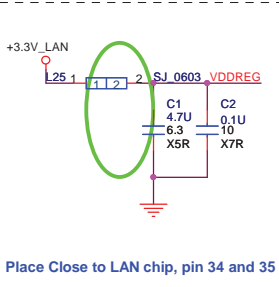
VA type: PIN28 作為MIC之偏壓
PIN31接A-GND

VB type: PIN31 作為MIC之偏壓
PIN28接CAP作為內部LDO
output 輸出濾波用





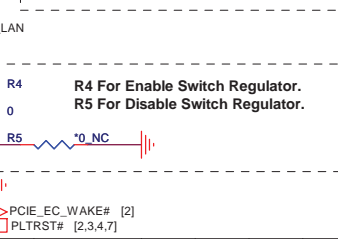
	10/100	Giga
L25, L27, L28	CS00002JB38	CW8AG601009



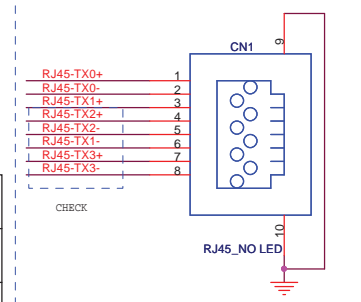
Icc33(+3.3V) ,typ=70mA
Icc10(+1.05V) ,typ=300mA

Check point:
1. LOM_CLK_REQ# and PCIE_WAKE# needs to be pull up by PCH side
2. PCIE_TX must have AC cap at PCH side

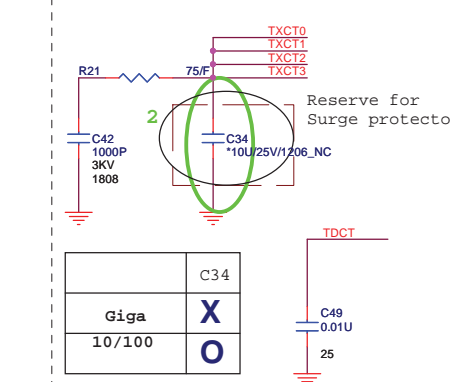
Isolate# is for power saving.
It needs to pull low when system state in S3, S4, and S5.
pull high when system at S0 state



RJ-45 Connector

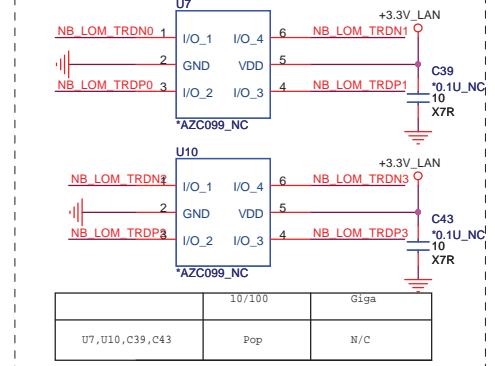


	Mode	L1	C3 C4	R2	C1 C2	R18	R4	R5
RTL8111E-VB	SWR	0	0	0	0	0	0	X
RTL8105E-VC	LDO	X	X	X	X	X	X	0
RTL8111E-VB	External	X	X	X	X	X	X	0
RTL8105E-VC	External	X	X	X	X	X	0	X

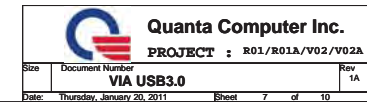


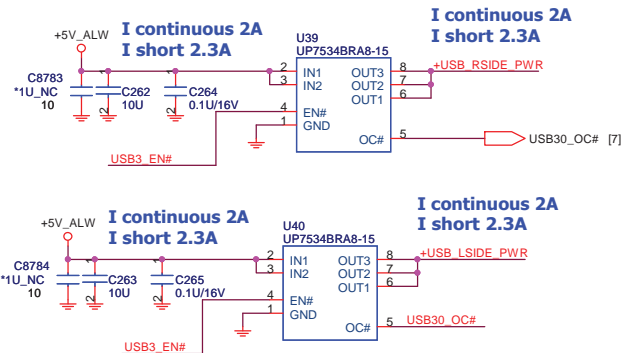
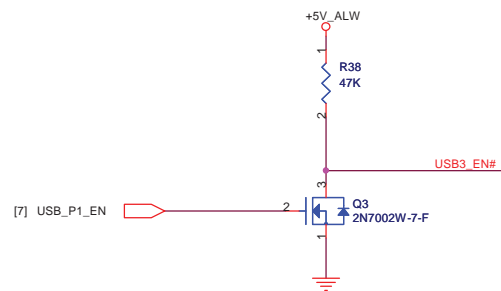
	C34
Giga	X
10/100	0

LAYOUT NOTE:
CAP CLOSE TO TRANSFORMER
one cap for each pin
Reserved for EMI.

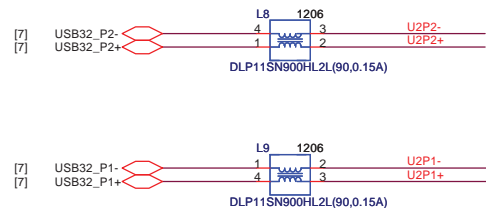


	10/100	Giga
U7, U10, C39, C43	Pop	N/C



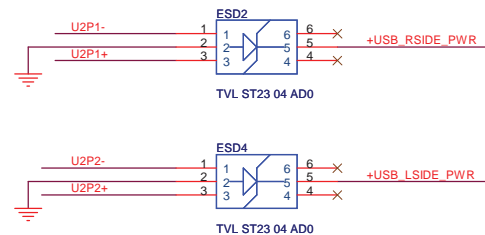


USB2.0 EMC



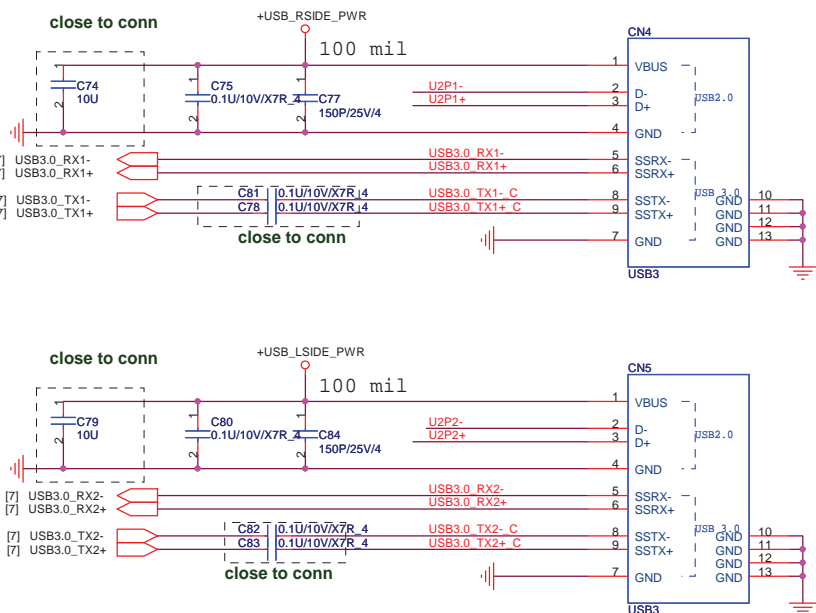
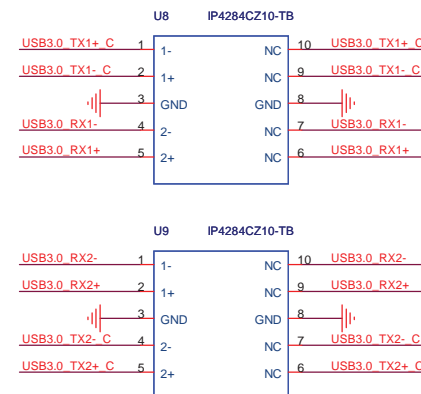
ESD Function

Place ESD diodes as close as USB connector.



USB3.0 EMC

ESD Function



Check FootPrint symbol

